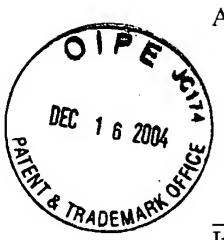
PTO/SB/21 (09-04)

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ENCLOSURES (Check all that apply)				
Total Number of Pages in This Submission	Attorney Docket Number	SON-2810		
TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Examiner Name	Not Yet Assigned		
	Art Unit	2811		
	First Named Inventor	Yoshitaka Kayukawa		
	Filing Date	August 26, 2003		
	Application Number	10/647,217-Conf. #1901		
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Fee Transmittal Form	Drawing(s)	After Allowance Communication to TC	
Fee Attached	Licensing-related Papers	Appeal Communication to Board of Appeals and Interferences	
X Preliminary Amendment	Petition	Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)	
After Final	Petition to Convert to a Provisional Application	Proprietary Information	
Affidavits/declaration(s)	Power of Attorney, Revocation Change of Correspondence Address	Status Letter	
Extension of Time Request Terminal Disclaimer		X Other Enclosure(s) (please Identify below):	
Express Abandonment Request	Request for Refund	Clean and Redline Substitute Specifications	
Information Disclosure Statement CD, Number of CD(s)			
Certified Copy of Priority Document(s) Landscape Table on CD			
Reply to Missing Parts/ Incomplete Application Remarks			
Reply to Missing Parts under 37 CFR 1.52 or 1.53			
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT			
Firm Name RADER, FISHMAN & GRAUER PLLC			
Signature			
Printed name Ronald P. Kananen			
December 16, 2004	Reg. No.	24,104	

Application No.: 10/647,217 Docket No.: SON-2810



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Yoshitaka Kayukawa et al.

Confirmation No.: 1901

Application No.: 10/647,217

Art Unit: 2811

Filed: August 26, 2003

Examiner: Violet McCoy

For: SEMICONDUCTOR INTEGRATED CIRCUIT

AND METHOD FOR TESTING SAME

FIRST PRELIMINARY AMENDMENT

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Prior to examination on the merits, please amend the above-identified U.S. patent application as follows: